

Category	Item
<input type="radio"/> Drafts	
<input type="radio"/> Pending	
<input type="radio"/> Active	<p>L1: (0) (stand adj by) same (isolat\$3 near3 (device circuit signal apparat</p> <p>L2: (0) (stand adj by) adj signal</p> <p>L3: (0) (stand adj by) near2 signal</p> <p>L4: (3703) ((stand adj by) standby) near2 signal</p> <p>L5: (415) ((stand adj by) standby) same (isolat\$3 near3 (device circuit si</p> <p>L6: (219) ((stand adj by) standby) with (isolat\$3 near3 (device circuit si</p> <p>L7: (80) ((stand adj by) standby) with (isolat\$3 near3 signal)</p> <p>L8: (33) ((stand adj by) standby) same (isolat\$3 near3 signal) same (isolo</p> <p>L9: (35) ((stand adj by) standby) same (isolat\$3 near3 signal) same (isolo</p>
<input type="radio"/> Failed	
<input type="radio"/> Saved	<p>(3059) (processor CPU microprocessor) and ((power adj2 supply) with bus) a</p> <p>(682) (((processor CPU microprocessor) and ((power adj2 supply) with bus)</p> <p>(14) (((processor CPU microprocessor) and ((power adj2 supply) with bus)</p> <p>(0) (processor CPU microprocessor) and ((power adj2 supply) with bus) sam</p> <p>(3) (((processor CPU microprocessor) and ((power adj2 supply) with bus)</p> <p>(12) (((processor CPU microprocessor) and ((power adj2 supply) with bus)</p> <p>(7) (((processor CPU microprocessor) and ((power adj2 supply) with bus)</p> <p>(3) (((processor CPU microprocessor) and ((power adj2 supply) with bus)</p> <p>(532614) (input output) adj2 pad buffer</p> <p>(136) ((input output) adj2 pad buffer) and ((internal near3 bus) with (pow</p> <p>(122) (((input output) adj2 pad buffer) and ((internal near3 bus) with (po</p> <p>(71) (((input output) adj2 pad buffer) and ((internal near3 bus) with (po</p> <p>(2477) ((processor CPU microprocessor) and ((power adj2 supply) with bus)</p> <p>(17) (((input output) adj2 pad buffer) and ((internal near3 bus) with (p</p> <p>(152) (power adj supply adj bus) and (isolat\$3 with (power adj supply))</p>

DB: JSPAT, US-PUB, EPO, JPO, GER, WENT, BM, 109

Default operator: OR

(((stand adj by) standby) same (isolat\$3 near3 signal) same (isolat\$3 near3 circuit device)

	u	l	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	3	4	5
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040008563	20040115	31	Semiconductor memory device with reduced power	365/230.03	365/230.06; 365/230.08		Kinoshita, Mitsuya	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20040004900	20040108	41	Semiconductor integrated circuit device with embedded	365/230.06			Dosaka, Katsumi	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20030206476	20031106	20	Low power consumption memory device having row-to-column	365/222	365/190		Joo, Yangsung	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20030060185	20030327	14	Wireless control and/or data acquisition system in an	455/344	455/130		Fisher, Richard J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20030058704	20030327	9	Zero power chip standby mode	365/200			Lovett, Simon J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20030048693	20030313	10	Zero power chip standby mode	365/226			Lovett, Simon J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 20030043679	20030306	10	Zero power chip standby mode	365/226			Lovett, Simon J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20030043678	20030306	10	Zero power chip standby mode	365/226			Lovett, Simon J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 20020191467	20021219	57	Semiconductor memory device	365/222			Matsumoto, Junko et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 20020080677	20020627	110	Semiconductor memory device	365/233	365/201; 365/230.03;		Watanabe, Naoya et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 20020033703	20020321	8	Insulation for a multi-power	324/551			Yin, Kuo-Hua et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>